

Amendments to the Claims:

Please amend the claims as set forth below.

1 – 3 (Cancelled)

4. (Previously Presented) In a device receiving multiplexed, packetized input data streams and outputting other multiplexed, packetized data streams, an output data stream mapper comprising:

an interface with an input data stream;

a packet processor configured to identify and route a selected plurality of related packets;

a memory retaining at least one stored format table, said stored format table having at least one stored set of input program numbers associated with at least one stored set of output program numbers, said memory further being configured to retain a current PAT;

a mapping processor configured to receive a packet from said packet processor, the packet being a the current PAT from the input data stream, said mapping processor being further configured to compare at least one set of input program numbers in the current PAT to said at least one stored set of input program numbers in said stored format table;

said mapping processor being further configured such that if the at least one set of input program numbers in the current PAT is the same as said at least one stored set of input program numbers in said stored format table, then an output data stream is output having said at least one stored set of output program numbers from said stored format table; and

said mapping processor being further configured such that if the at least one set of input program numbers in the current PAT is not the same as said at least one stored set of input

program numbers in ~~the~~ said stored format table, then an output data stream is output having at least one reassigned output program numbers.

5. (Previously Presented) The output data stream mapper of claim 4 wherein said mapping processor is further configured such that if the input program numbers in the current PAT are not the same as said at least one stored set of input program numbers in said stored format table, then said mapping processor identifies another stored set of input program numbers in said stored format table in said memory, said another stored set of input program numbers having input program numbers that match the input program numbers in said current PAT, and another output data stream is output having reassigned output program numbers, said reassigned output program numbers being retrieved from another stored set of output program numbers associated in said stored format table with said another stored set of input program numbers.

6. (Previously Presented) The output data stream mapper of claim 4 wherein said mapping processor is further configured such that if the input program numbers in the current PAT are not the same as any stored set of input program numbers in the stored format table, then said mapping processor is configured to generate new program numbers and then output another output data stream having reassigned output program numbers, said reassigned output program numbers being said newly generated program numbers.

7. (Original) The output data stream mapper of claim 6 wherein said newly generated program numbers are generated by random number generation.

8. (Original) The output data stream mapper of claim 6 wherein said newly generated program numbers are generated by incrementing numbers.

9. (Previously Presented) The output data stream mapper of claim 4 wherein said mapping processor is further configured to receive a packet from said packet processor, the packet being the current PAT from the input data stream, said mapping processor being further configured to compare an input PMT PID in said current PAT to a known PMT PID in said stored format table;

said mapping processor being further configured such that if the input PMT PID in the current PAT is the same as the input PMT PID in the stored format table, then another data stream is output having output PMT PID from the stored format table;

said mapping processor being further configured such that if the input PMT PIDs in the current PAT are not the same as the input program numbers in the stored format table, then another data stream is output having reassigned output PMT PIDs; and

said reassigned output PMT PIDs being from said stored format table.

10. (Original) The output data stream mapper of claim 9 wherein said mapping processor is further configured such that if the input PMT PIDs in the current PAT are not the same as the input PMT PIDs in the stored format table, then said mapping processor is configured to generate new PMT PIDs and then output another data stream having reassigned output PMT PIDs, said reassigned output PMT PIDs being said newly generated PMT PIDs.

11. (Original) The output data stream mapper of claim 10 wherein said newly generated PMT PIDs are generated by random number generation.

12. (Original) The output data stream mapper of claim 10 wherein said newly generated PMT PIDs are generated by incrementing numbers.

13. (Previously Presented) The output data stream mapper of claim 4 wherein said mapping processor is further configured such that if the input program numbers in the

current PAT are not the same as the input program numbers in the stored format table, then said mapping processor is further configured to output another output data stream having reassigned output PIDs within the PMTs.

14. (Original) The output data stream mapper of claim 13 wherein said reassigned output PIDs within the PMTs are newly generated by said mapping processor.

15. (Original) The output data stream mapper of claim 14 wherein said newly generated output PIDs within the PMTs are generated by random number generation.

16. (Original) The output data stream mapper of claim 14 wherein said newly generated output PIDs within the PMTs are generated by incrementing numbers.

17. (Original) The output data stream mapper of claim 4 wherein said packet processor is further configured to re-timestamp output packetized data streams.

18. (Original) The output data stream mapper of claim 4 wherein said mapping processor is further configured for error correction.

19. (Original) The output data stream mapper of claim 18 wherein said error correction is by repeated unrecognition of a single input program number.

20. (Original) The output data stream mapper of claim 18 wherein said error correction is by unrecognition of at least two different input program numbers.

21. (Previously Presented) The device of claim 4 wherein said data streams include high definition content.

22. (Previously Presented) The device of claim 4 wherein said output data streams are statistically multiplexed.

23. (Previously Presented) The device of claim 4 further comprising a network interface with said mapping processor.

24. (Previously Presented) The device of claim 4 further comprising a network interface with said packet processor.
25. (Previously Presented) The device of claim 4 wherein said memory is updated by a received packet processor table.
26. (Previously Presented) The device of claim 4 wherein said stored format table stores program numbers and transport streams.
27. (Previously Presented) The device of claim 4 wherein each of said stored format tables is comprised of a single table having stored input program numbers, stored input transport streams, stored output program numbers and stored output transport streams.
28. (Previously Presented) The device of claim 4 wherein said each of said stored format tables is comprised of an input table and an output table, said input table having stored input program numbers and stored input transport streams and said input table being associated with said output table, said output table having stored output program numbers and stored output transport streams.
29. (Previously Presented) The device of claim 4 further comprising a display notifying a human operator when the input program numbers are not found in said stored format table.
30. (Previously Presented) The device of claim 4 further including a stored preference table.
31. (Previously Presented) The device of claim 4 wherein said memory further includes a temporary storage of at least one incoming PMT.
32. (Previously Presented) The device of claim 4 wherein said processor is configured to check for a unique PID for each input PMT.

33. (Previously Presented) The device of claim 32 wherein said mapper processor is further configured to assign new PIDs for input PMTs such that each output PMT has a unique PID.

34. (Previously Presented) The device of claim 4 wherein said mapping processor is further configured to check if an incoming program number corresponds to a unique output program number.

35. (Previously Presented) The device of claim 34 wherein said mapping processor is further configured to assign a unique output program number for each incoming program number.

36. (Previously Presented) The device of claim 4 further comprising a memory for storing at least one of a newly generated PAT or a newly generated PMT.

37. (Previously Presented) The device of claim 4 further comprising a capacity limit on a number of programs.

38. (Previously Presented) The device of claim 4 further comprising a program number remapping table.

39. (Previously Presented) The device of claim 38 wherein said program number remapping table includes an item number, an input designation, an output number and an activity designation.

40. (Previously Presented) The device of claim 4 further comprising a PID remapping table.

41. (Previously Presented) The device of claim 40 wherein said PID remapping table includes an item number, an input designation, an output number, an output designation and an activity indicator.

42. (Previously Presented) The device of claim 38 wherein said mapping processor is further configured to assign unique PID numbers for each output data stream.